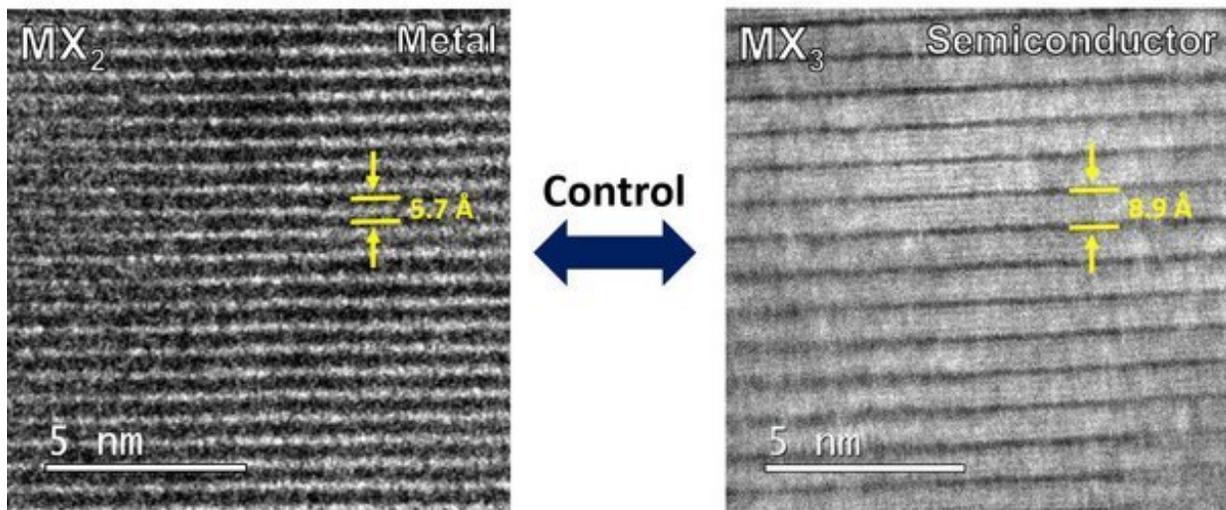


# Producing materials to help break the electronics scaling limit

November 13 2020



Credit: Eindhoven University of Technology

Ph.D. candidate Saravana Balaji Basuvalingam at the TU/e Department of Applied Physics has developed a new approach to grow, in a controlled and effective way, a library of so-called "TMC materials" with various properties at low-temperatures. This brings the world one step closer to moving beyond silicon-based semiconductor devices.

As the amount of data produced by humanity grows exponentially, with it comes the demand for smaller, faster and cheaper electronic devices to

process these data. To address this demand, the [semiconductor industry](#) is continuously searching for ways to scale devices below 3 nm. This scale is an important barrier for the industry, because it is near the limits of what can be done with silicon (Si), the most commonly used material for electric circuits. Below that scale, silicon-based devices often suffer from poor performance.

Certain 2-D materials, of which graphene may be the most well-known example, offer the promise to solve this scaling issue. Characteristic of these materials is that each layer of atoms is free-standing on the layer of atoms below, without any bonds connecting the layers. The 2-D materials classified as transition metal chalcogenides (TMCs) has gained attention for their excellent electrical properties and thickness of less than 1 nm, enabling [device](#) performances similar to Si-based devices and a great potential for scaling.

However, several synthesis limitations restrict the implementation of TMCs in industry in a cost-effective way. Basuvalingam's research aimed to solve most of these technical limitations, such as growing the TMCs in a large enough area, at low temperature and with good material property control. To do so, he used a thin-film approach known as the [atomic layer deposition](#) (ALD) method. ALD is one of the prominent methods to facilitate the reduction of device dimensions in the semiconductor industry, and the method had already been studied for TMCs which exhibit semiconducting properties.

Basuvalingam was the first to study ALD to grow 2-D TMCs with both semiconducting and metallic properties in a large area at low temperatures, and the first to achieve control over TMC material composition using thin-film synthesis. His approach also made it possible to grow TMCs in a 200mm wafer and to achieve control of the material properties between metallic and semiconducting.

His work expands the library of materials that can be grown using a thin-film method and helps us get one step closer to smaller, more cost-effective electronic devices made of 2-D materials.

**More information:** Atomic layer deposition of low-dimensional transition metal di- and tri-chalcogenides and their heterostructures.  
[research.tue.nl/en/publication ... ansition-metal-di-an](https://research.tue.nl/en/publication/.../transition-metal-di-an)

Provided by Eindhoven University of Technology

Citation: Producing materials to help break the electronics scaling limit (2020, November 13)  
retrieved 19 September 2024 from  
<https://phys.org/news/2020-11-materials-electronics-scaling-limit.html>

This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.