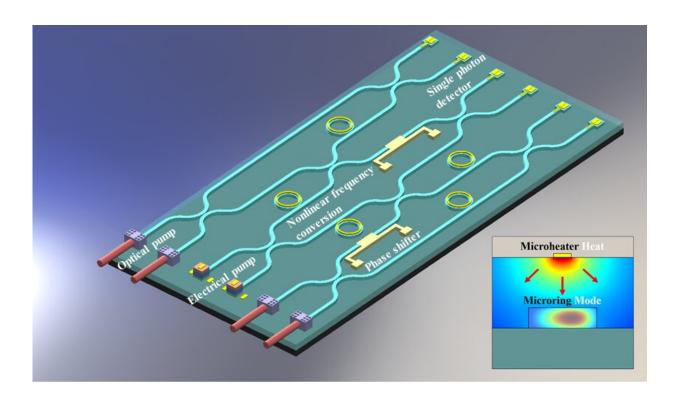


Tunable optical chip paves way for new quantum devices

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Researchers created the first thermally tunable optical switch using a silicon carbide-on-insulator platform. The schematic image shows their concept for a quantum photonics integrated circuit chip that includes the circular microring resonators and microheaters reported in Optics Letters. The inset shows the temperature and electric field distributions at the cross-section of a microring resonator heated by a microheater. Credit: Ali Adibi, Georgia Institute of Technology



Researchers have created a silicon carbide (SiC) photonic integrated chip that can be thermally tuned by applying an electric signal. The approach could one day be used to create a large range of reconfigurable devices such as phase-shifters and tunable optical couplers needed for networking applications and quantum information processing.

Although most optical and computer chips are made of silicon, there is increasing interest in SiC because it exhibits better thermal, electrical and <u>mechanical properties</u> than silicon while also being biocompatible and operating at wavelengths from the visible to infrared.

In The Optical Society (OSA) journal *Optics Letters*, researchers led by Ali Adibi from the Georgia Institute of Technology detail how they integrated a microheater and an <u>optical device</u> called a microring <u>resonator</u> onto a SiC chip. The accomplishment represents the first fully integrated and thermally tunable SiC optical switch that operates at nearinfrared wavelengths.

"Devices such as the one we demonstrate in this work can be used as building-blocks for next generation quantum information processing devices and to create biocompatible sensors and probes," said the paper's first author Xi Wu.

SiC is particularly attractive for quantum computing and communication applications because it has defects that can be optically controlled and manipulated as quantum bits, or qubits. Quantum computing and communication promises to be significantly faster than traditional computing at solving certain problems because data is encoded in qubits that can be in any combination of two states at once, allowing many processes to be performed simultaneously.

Wafer-level manufacturing



The new work builds on the researchers' previous development of a platform called crystalline SiC-on-insulator that overcomes some of the fragility and other drawbacks of previously reported SiC platforms while providing an easy and reliable route for integration with electronic devices.

"The SiC-on-insulator platform our group pioneered is similar to the silicon-on-insulator technology widely used in semiconductor industry for a variety of applications," said Tianren Fan, member of the research team. "It enables wafer-level manufacturing of SiC devices, paving the way toward commercialization of integrated photonic <u>quantum</u> information processing solutions based on SiC," said Ali A. Eftekhar, member of the research team.

Fully exploiting the new platform's unique capabilities required developing the ability to tune its optical properties so that a <u>single chip</u> -based structure can be used to provide different functions. The researchers accomplished this by using the thermo-optic effect in which changing a material's temperature modifies its optical properties, such as refractive index.

They began by fabricating tiny ring-shaped optical cavities, or microring resonators, using the crystalline SiC-on-insulator technology. In each resonator, light at certain wavelengths, called its resonance wavelengths, traveling around the ring will build up strength through constructive interference. The resonator then can be used to control the amplitude and phase of the light in a waveguide coupled to it. To create a tunable resonator with a high degree of control, the researchers fabricated electric heaters on top of the microrings. When an electric current is applied to the integrated microheater, it locally increases the temperature of the SiC microring and thus changes its resonant wavelengths thanks to the thermo-optic effect.



Testing the integrated device

The researchers tested the performance of the fabricated integrated microring resonators and microheaters by applying different levels of electrical power and then measuring the optical transmission of the waveguide coupled to the microring resonator. Their results showed that it is possible to achieve high-quality resonators with low-power thermal tunability through a robust device that can be manufactured using existing semiconductor foundry processes.

"Combined with other unique features of our crystalline SiC-oninsulator platform, these high-quality devices have the basic requirements for enabling new chip-scale devices that operate in a wide range of wavelengths," said Ali Adibi, the team leader. "This chip-scale tunability is essential for performing quantum operations necessary for quantum computing and communication. In addition, because of the biocompatibility of SiC, it could be very useful for in vivo biosensing."

The researchers are now working to build elements with the crystalline SiC-on-insulator platform for quantum photonic integrated circuits, including on-chip pump lasers, single photon sources and single photon detectors that could be used with the tunable microring resonator to create a fully functional chip for advanced optical <u>quantum computing</u>.

This work is the result of three years of extensive research in forming a reliable hybrid platform with considerably improved SiC material properties and using it for forming innovative devices. Xi Wu, Tianren Fan, and Ali A. Eftekhar in Ali Adibi's research group contributed immensely to this work. Hesam Moradinejad, a former member of Adibi's researcher group, also contributed to the platform development (published earlier). This work was primarily funded by the Air Force Office of Scientific Research (AFOSR) under grant number FA9550-15-1-0342 (G. Pomrenke).



More information: Xi Wu et al, High-Q microresonators integrated with microheaters on a 3C-SiC-on-insulator platform, *Optics Letters* (2019). DOI: 10.1364/OL.44.004941

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