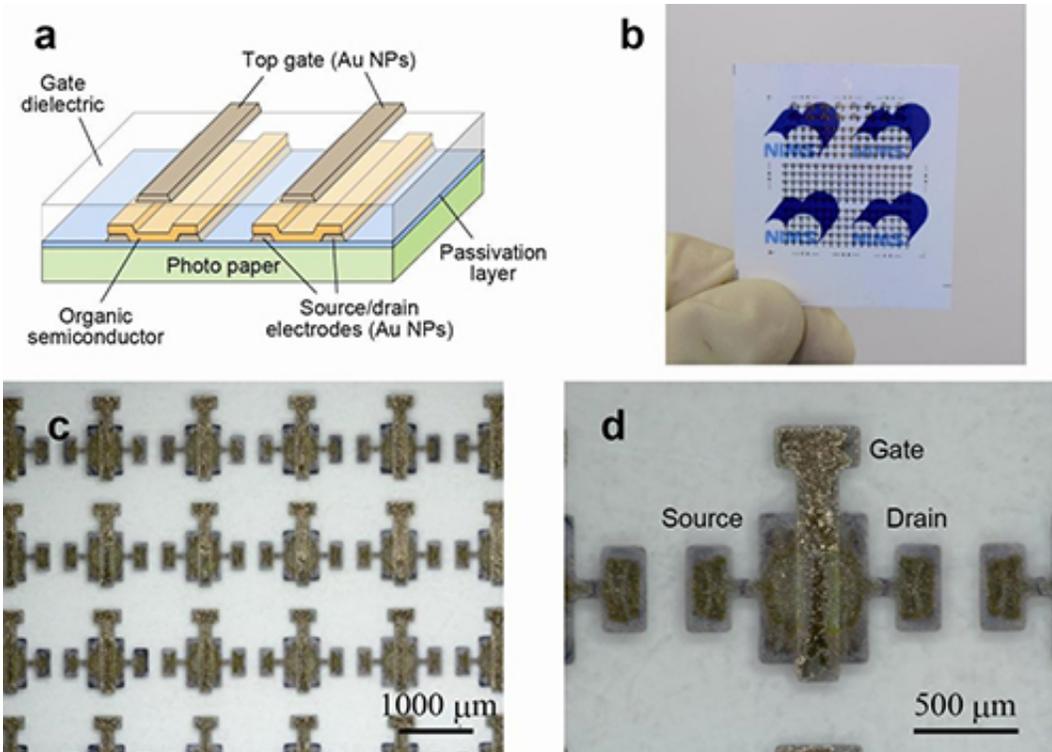


A cool approach to flexible electronics

July 10 2014



Fully printed organic thin film transistors (OTFTs) on a paper substrate. (a) Schematic of the device structure for a fully printed OTFT on paper. (b) Arrays of fully printed OTFTs fabricated on a paper substrate inkjet printed with the NIMS logo before adding the device. (c) An optical microscope image of fully-printed OTFT arrays. (d) A magnified optical microscope image of the individual device. Arrays of fully printed organic thin film transistors fabricated on paper substrates that had the the NIMS logo ink jet printed on before processing.

A nanoparticle ink that can be used for printing electronics without high-

temperature annealing presents a possible profitable approach for manufacturing flexible electronics.

Printing [semiconductor devices](#) is considered to provide low-cost high performance flexible electronics that outperforms the amorphous silicon [thin film transistors](#) currently limiting developments in display technology. However the nanoparticle inks developed so far have required annealing, which limits them to substrates that can withstand high temperatures, ruling out a lot of the flexible plastics that could otherwise be used. Researchers at the National Institute for Materials Science and Okayama University in Japan have now developed a nanoparticle ink that can be used with [room-temperature](#) printing procedures.

Developments in thin film transistors made from [amorphous silicon](#) have provided wider, thinner displays with higher resolution and lower energy consumption. However further progress in this field is now limited by the low response to applied electric fields, that is, the low field-effect mobility. Oxide semiconductors such as InGaZnO (IGZO) offer better performance characteristics but require complicated fabrication procedures.

Nanoparticle inks should allow simple low-cost manufacture but the [nanoparticles](#) usually used are surrounded in non-conductive ligands – molecules that are introduced during synthesis for stabilizing the particles. These ligands must be removed by annealing to make the ink conducting. Takeo Minari, Masayuki Kanehara and colleagues found a way around this difficulty by developing nanoparticles surrounded by planar aromatic molecules that allow charge transfer.

The gold nanoparticles had a resistivity of around $9 \times 10^{-6} \Omega \text{ cm}$ – similar to pure gold. The researchers used the nanoparticle ink to print organic thin film transistors on a flexible polymer and a paper substrate

at room temperature, producing devices with mobilities of 7.9 and 2.5 $\text{cm}^2 \text{V}^{-1} \text{s}^{-1}$ for polymer and paper respectively – figures comparable to IGZO devices.

As the researchers conclude in their report of the work, "This room temperature printing process is a promising method as a core technology for future semiconductor devices."

More information: Minari, T., Kanehara, Y., Liu, C., Sakamoto, K., Yasuda, T., Yaguchi, A., Tsukada, S., Kashizaki, K. and Kanehara, M. (2014), "Room-Temperature Printing of Organic Thin-Film Transistors with π -Junction Gold Nanoparticles." *Adv. Funct. Mater.*..
doi: 10.1002/adfm.201400169

Provided by International Center for Materials Nanoarchitectonics

Citation: A cool approach to flexible electronics (2014, July 10) retrieved 18 April 2024 from <https://phys.org/news/2014-07-cool-approach-flexible-electronics.html>

This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.