

Samsung Standardizes on Synopsys DesignWare USB IP

August 31 2004

Multi-Year License Agreement Gives Samsung Access to Broad Portfolio of DesignWare IP

Synopsys, Inc., the world leader in semiconductor design software, today announced that [Samsung](#) Electronics Co. Ltd., a leader in advanced [semiconductor technology](#), has signed a multi-year license agreement for Synopsys' DesignWare(R) intellectual property (IP). Under the terms of the agreement, Samsung is licensing the DesignWare Cores IP portfolio, which includes the industry-leading PCI Express and USB families of digital cores and analog PHYs. The first DesignWare IP Core Samsung will use in its devices under the license agreement is the USB 2.0 PHY core. By standardizing on Synopsys' certified Hi-Speed USB 2.0 PHY core, Samsung will more quickly deliver flexible, cost-effective [USB 2.0-enabled](#) products based on 130 nanometer (nm) and 90-nm process technology to its ASIC customers.

"Outsourcing select IP lets our engineers focus on the value-added portions of the design such as quality, features and performance," said K. H. Kim, vice president of ASIC business at System LSI division, Samsung Electronics. "This agreement with Synopsys will add industry-leading PCI Express and USB technology to the variety of proven IP cores provided by Samsung's ASIC business. Based on Samsung's diverse IP portfolio of high performance and feature-rich ASIC products, we can fully satisfy the high demands of our customers."

"We've enjoyed a long and successful relationship with Samsung and are proud to be chosen as their IP provider, delivering the high-quality IP and dependability that they demand," said John Chilton, senior vice president and general manager of Synopsys' Solutions Group. "With this multi-year agreement, we can look forward to extending our relationship with Samsung as a design partner."

About DesignWare Cores

DesignWare Cores provide system designers with silicon proven, digital and analog connectivity IP for some of the world's most recognized products including communications processors, routers, switches, game consoles, digital cameras, computers and computer peripherals. The DesignWare IP family includes industry leading connectivity IP Cores and Verification IP (e.g., USB 1.1, USB 2.0, USB 2.0 PHY, USB 2.0 OTG, USB 2.0 OTG PHY, PCI, PCI-X(R), PCI Express(TM), Ethernet ...), AMBA(TM) on-chip bus (logic, peripherals, verification IP) and microcontrollers (8051, 6811). Provided as synthesizable RTL source code or in GDS format, these cores enable designers to create innovative, cost-effective systems-on-chip and embedded systems. Synopsys provides flexible licensing options for the DesignWare Cores. Each Core can be licensed individually, on a fee-per-project basis or users can opt for the Volume Purchase Agreement, which enables them to license all the cores under one simple agreement. For more information on DesignWare IP, visit <http://www.synopsys.com/designware>.

Source: Synopsys, Inc.

Citation: Samsung Standardizes on Synopsys DesignWare USB IP (2004, August 31) retrieved 20 September 2024 from

<https://phys.org/news/2004-08-samsung-standardizes-synopsys-designware-usb.html>

This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.