

Photonic chips can calculate optimal shape of light for next-gen wireless systems

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Chip. Credit: Politecnico di Milano

Optical wireless may no longer have any obstacles. A study by Politecnico di Milano, conducted together with Scuola Superiore Sant'Anna in Pisa, the University of Glasgow and Stanford University, and <u>published</u> in *Nature Photonics*, has made it possible to create photonic chips that mathematically calculate the optimal shape of light to best pass through any environment, even one that is unknown or



changing over time.

The problem is well known: light is sensitive to any form of obstacle, even very small ones. Think, for example, of how we see objects when looking through a frosted window or simply when our glasses get foggy. The effect is quite similar on a beam of light carrying <u>data streams</u> in optical wireless systems: the information, while still present, is completely distorted and extremely difficult to retrieve.

The devices developed in this research are small silicon chips that serve as smart transceivers: working in pairs, they can automatically and independently 'calculate' what shape a <u>beam of light</u> needs to be in order to pass through a generic environment with <u>maximum efficiency</u>. And that's not all: they can also generate multiple overlapping beams, each with its own shape, and direct them without them interfering with each other; in this way, the transmission capacity is significantly increased, just as required by next-generation wireless systems.

"Our chips are mathematical processors that make calculations with light very quickly and efficiently, almost with no <u>energy consumption</u>. The optical beams are generated through simple algebraic operations, essentially sums and multiplications, performed directly on the light signals and transmitted by micro-antennas directly integrated on the chips. This technology offers many advantages: extremely easy processing, high energy efficiency, and an enormous bandwidth exceeding 5000 GHz," Francesco Morichetti, Head of the Photonic Devices Lab of Politecnico di Milano, explains.

"Today, all information is digital, but in fact, images, sounds, and all data are inherently analog. Digitization does allow for very complex processing, but as the volume of data increases, these operations become increasingly less sustainable in terms of energy and computation. Today, there is great interest in returning to analog technologies, through



dedicated circuits (analog co-processors) that will serve as enablers for the 5G and 6G wireless interconnection systems of the future. Our chips work just like that," Andrea Melloni, Director of Polifab, Politecnico di Milano's micro and nanotechnology center, says.

"Analog computing using optical processors is crucial in numerous application scenarios that include mathematical accelerators for neuromorphic systems, high-performance computing (HPC) and <u>artificial intelligence</u>, quantum computers and cryptography, advanced localization, positioning and sensor systems, and in general, in all systems where the processing of large amounts of data at very high speed is required," adds Marc Sorel, Professor of Electronics at the TeCIP Institute (Telecommunications, Computer Engineering, and Photonics Institute) of Scuola Superiore Sant'Anna.

More information: SeyedMohammad SeyedinNavadeh et al, Determining the optimal communication channels of arbitrary optical systems using integrated photonic processors, *Nature Photonics* (2023). DOI: 10.1038/s41566-023-01330-w

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