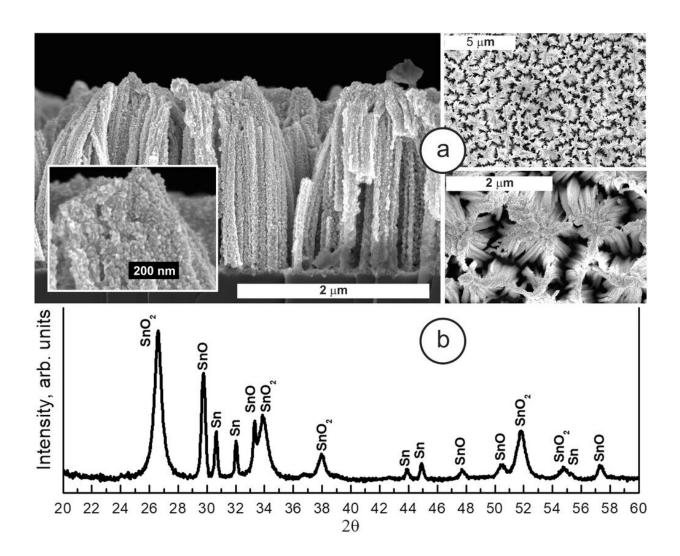


Functional surface refinement: Targeted control of growth dynamics of finest tin layers

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SEM micrographs of the formed SnO_2 -SiNWs composite. The left panel gives a cross-sectional view of the SiNWs matrix. The right panel gives a planar SEM



image of the SiNWs array. b) XRD pattern of SnO^x Credit: *Small* (2023). DOI: 10.1002/smll.202206322

Nanometer-scale coatings with functional materials play an important role in many sensory, electronic and photonic applications. An international team of researchers, coordinated by Leibniz IPHT in Jena, Germany, has succeeded for the first time in observing novel growth effects of tin coatings on silicon nanometer-structured surfaces.

With the knowledge gained, the <u>chemical composition</u> of deposited <u>thin</u> <u>films</u> can be precisely controlled and monitored in the future, opening up new applications in the fields of biophotonics, energy generation or mobility. The results were published in the journal *Small*.

Tin-containing layers are in demand for a wide variety of electronic parts and components in the electrical industry as well as in sensor technology or photovoltaics. Researchers from the Leibniz Institute of Photonic Technology (Leibniz IPHT) investigated the development process of nanoscale tin layers together with scientists from Germany, Russia and Great Britain and summarized their results in the journal *Small*.

The starting material for the observed growth processes of tin-containing thin films are ultra-thin silicon-based structures in the form of nanowires with a diameter of less than 100 nanometers. In experimental studies, the researchers were able to demonstrate for the first time a specific distribution effect of tin along these silicon nanostructures. Tin-containing layers with different degrees of oxidation were formed along the entire length of the semiconductor nanowires by means of metal-organic chemical <u>vapor deposition</u> at a deposition temperature of 600° Celsius.



"By understanding how tin coatings grow and which factors influence this growth process, we create the conditions for specifically controlling coating processes. This allows surfaces to be refined very precisely and to be equipped with desired functional properties at previously defined positions," explains Dr. Vladimir Sivakov, head of the Silicon Nanostructures Group at Leibniz IPHT, who investigated and discovered the growth mechanisms together with his team.

Applications of ultra-thin tin layers

Nanometer-thin coatings with tin enable specific optical and <u>electrical</u> <u>properties</u> and allow, among other things, to further improve the research and development of optical and biophotonic methods.

Tin layers can be used as UV-SERS-active surfaces in surface-enhanced Raman scattering (SERS) spectroscopy, which can be applied to determine the molecular fingerprint of biological samples using SERSactive metal nanostructures. In addition, there are areas of application in gas sensors in which tin reacts to gases as a highly sensitive layer. Application scenarios in high-performance lithium-ion batteries for electromobility and thermal energy storage are also conceivable, in which tin-coated anodes ensure high electronic conductivity.

Mechanisms and growth dynamics of tin-containing layers

The researchers investigated the growth dynamics of the observed tinbased layers on nanostructured surfaces using microscopic and spectroscopic methods. In contrast to planar and unstructured silicon surfaces, on which the deposition took place homogeneously, the surfaces of the semiconductor nanowires were covered with tincontaining crystals of different sizes and shapes over the entire length.



The results presented in the journal show the formation of different tin oxide phases along the nanostructured silicon surfaces, which could be identified with tin dioxide (SnO_2) in the upper part, tin monoxide (SnO) in the middle part and with metallic tin (Sn) in the lower part.

The amount and distribution of the formed metallic Sn and its SnO and SnO_2 oxides can be explained and effectively controlled by the length, diameter, porosity, and spacing of the silicon-based semiconductor nanostructures. In addition to these geometrical parameters, the researchers were able to reveal the formation of hydrocarbon-containing by-products as reducing agents for tin oxide reduction as another factor influencing the distribution of the formed tin layers along the semiconductor nanostructures.

The thermal conductivity of the silicon structures and thus the temperature distribution along the nanowires during the hightemperature vapor deposition can also have an influence on the formation of different tin oxide phases.

More information: Sergey Turishchev et al, Spectromicroscopy Studies of Silicon Nanowires Array Covered by Tin Oxide Layers, *Small* (2023). <u>DOI: 10.1002/smll.202206322</u>

Poting Liu et al, Nanostructured Silicon Matrix for Materials Engineering, *Small* (2023). DOI: 10.1002/smll.202206318

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