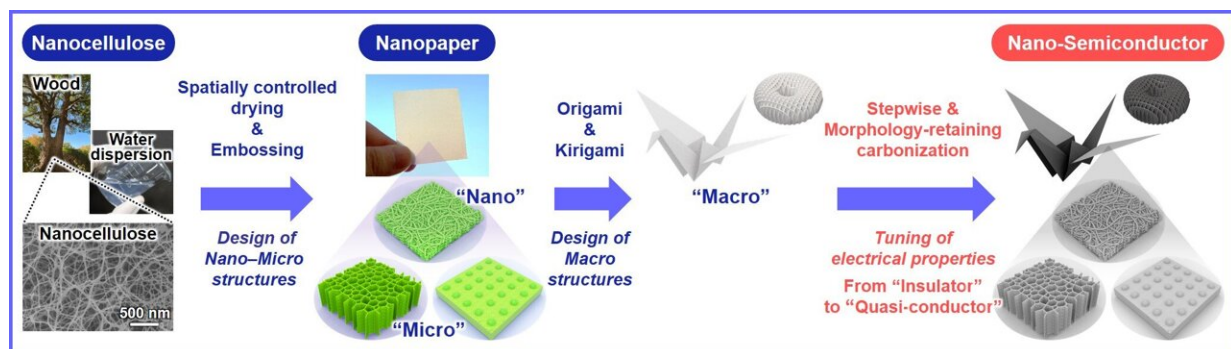


Electronics can grow on trees thanks to nanocellulose paper semiconductors

April 26 2022



Schematic diagram of the preparation of the wood nanocellulose-derived nano-semiconductor with customizable electrical properties and 3D structures. Credit: 2022 Koga et al. Nanocellulose paper semiconductor with a 3D network structure and its nano–micro–macro trans-scale design. *ACS Nano*

Semiconducting nanomaterials with 3D network structures have high surface areas and a lot of pores that make them excellent for applications involving adsorbing, separating, and sensing. However, simultaneously controlling the electrical properties and creating useful micro- and macro-scale structures, while achieving excellent functionality and end-use versatility, remains challenging. Now, Osaka University researchers, in collaboration with The University of Tokyo, Kyushu University, and Okayama University, have developed a nanocellulose paper semiconductor that provides both nano–micro–macro trans-scale

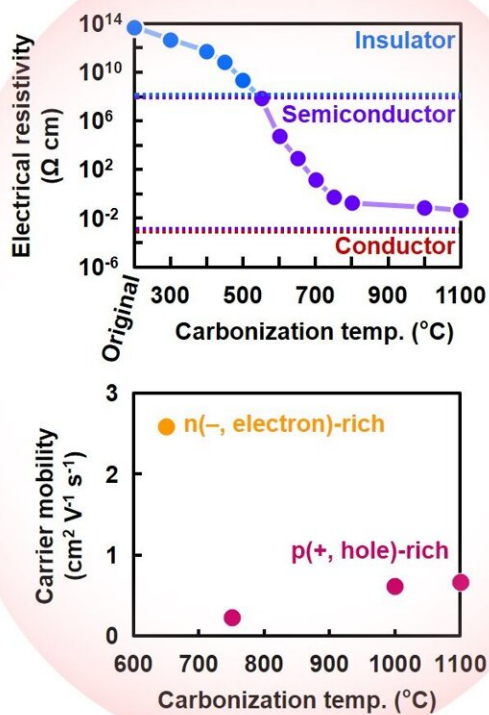
designability of the 3D structures and wide tunability of the electrical properties. Their findings are published in *ACS Nano*.

Cellulose is a natural and easy to source material derived from wood. Cellulose nanofibers (nanocellulose) can be made into sheets of flexible nanocellulose paper (nanopaper) with dimensions like those of standard A4. Nanopaper does not conduct an electric current; however, heating can introduce conducting properties. Unfortunately, this exposure to heat can also disrupt the nanostructure.

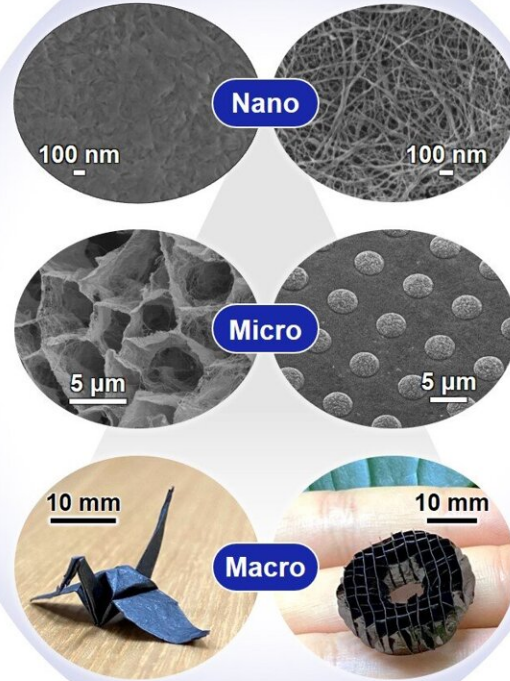
The researchers have therefore devised a treatment process that allows them to heat the nanopaper without damaging the structures of the paper from the nanoscale up to the macroscale.

"An important property for the nanopaper [semiconductor](#) is tunability because this allows devices to be designed for specific applications," explains study author Hirotaka Koga. "We applied an iodine treatment that was very effective for protecting the nanostructure of the nanopaper. Combining this with spatially controlled drying meant that the pyrolysis treatment did not substantially alter the designed structures and the selected temperature could be used to control the [electrical properties](#)."

a Wide and systematic tunability of electrical properties



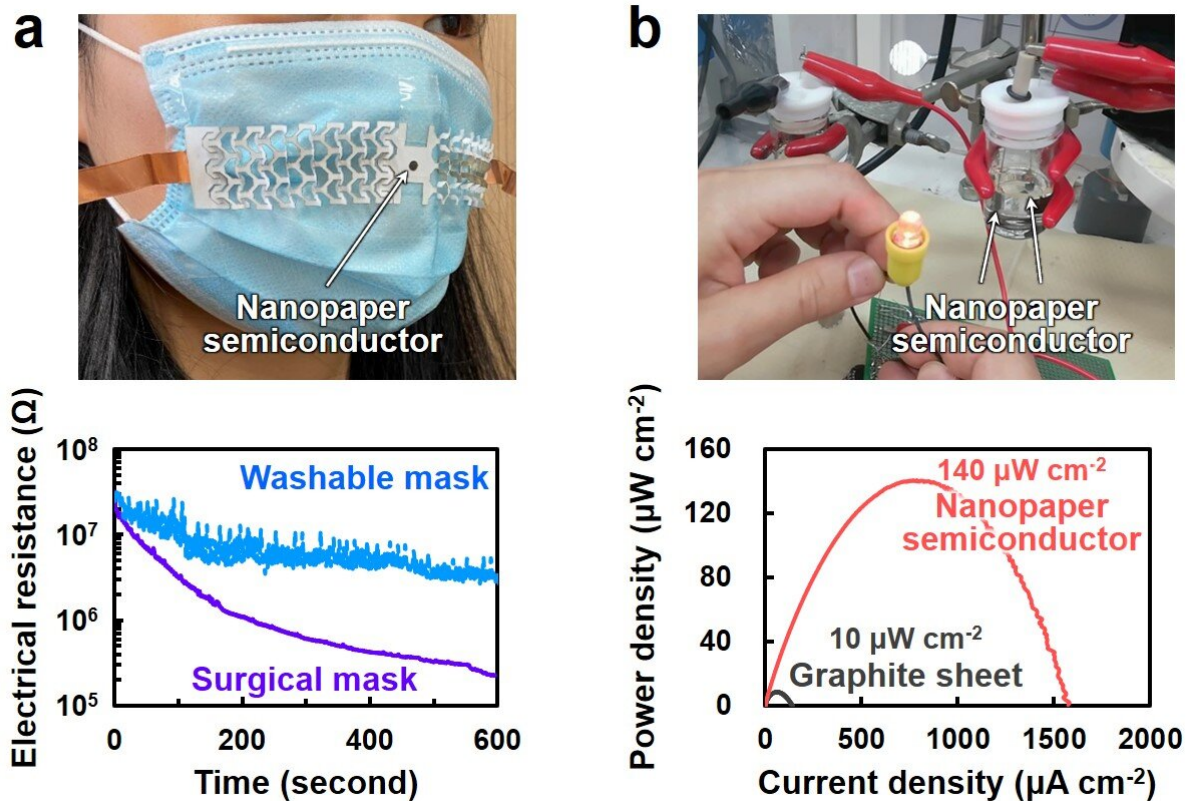
b Trans-scale designability of 3D structures



(a) Wide and systematic tunability of the electrical properties and (b) nano-micro-macro trans-scale designability of 3D structures of the as-prepared semiconductor (denoted the nanopaper semiconductor). Credit: 2022 Koga et al. Nanocellulose paper semiconductor with a 3D network structure and its nano–micro–macro trans-scale design. *ACS Nano*

The researchers used origami (paper folding) and kirigami (paper cutting) techniques to provide playful examples of the flexibility of the nanopaper at the macrolevel. A bird and box were folded, shapes including an apple and snowflake were punched out, and more intricate structures were produced by laser cutting. This demonstrated the level of detail possible, as well as the lack of damage caused by the [heat treatment](#).

Examples of successful applications showed nanopaper semiconductor sensors incorporated into wearable devices to detect exhaled moisture breaking through facemasks and moisture on the skin. The nanopaper semiconductor was also used as an electrode in a glucose biofuel cell and the energy generated lit a small bulb.



Device demonstrations using the nanopaper semiconductor. (a) Wearable sensor for monitoring human exhalation-derived water vapor leaked from facial masks. Washable mask: Sensor response pulses corresponding to respiration were observed, indicating a water vapor leak from the washable mask. Surgical mask: Only a gradual decrease in sensor resistance was observed, indicating the effective water vapor capture of the surgical mask. (b) Glucose biofuel cell for energy generation. The nanopaper semiconductor demonstrated a power density 14 times higher than that of a commercial graphite sheet, and led to illumination

of a red LED. Credit: 2022 Koga et al. Nanocellulose paper semiconductor with a 3D network structure and its nano–micro–macro trans-scale design. *ACS Nano*

"The structure maintenance and tunability that we have been able to show is very encouraging for the translation of nanomaterials into practical devices," says Associate Professor Koga. "We believe that our approach will underpin the next steps in sustainable electronics made entirely from plant materials."

The article, "Nanocellulose [paper](#) semiconductor with a 3D network structure and its nano–micro–macro trans-scale design," was published in *ACS Nano*.

More information: Nanocellulose paper semiconductor with a 3D network structure and its nano–micro–macro trans-scale design, *ACS Nano* (2022). [DOI: 10.1021/acsnano.1c10728](https://doi.org/10.1021/acsnano.1c10728)

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