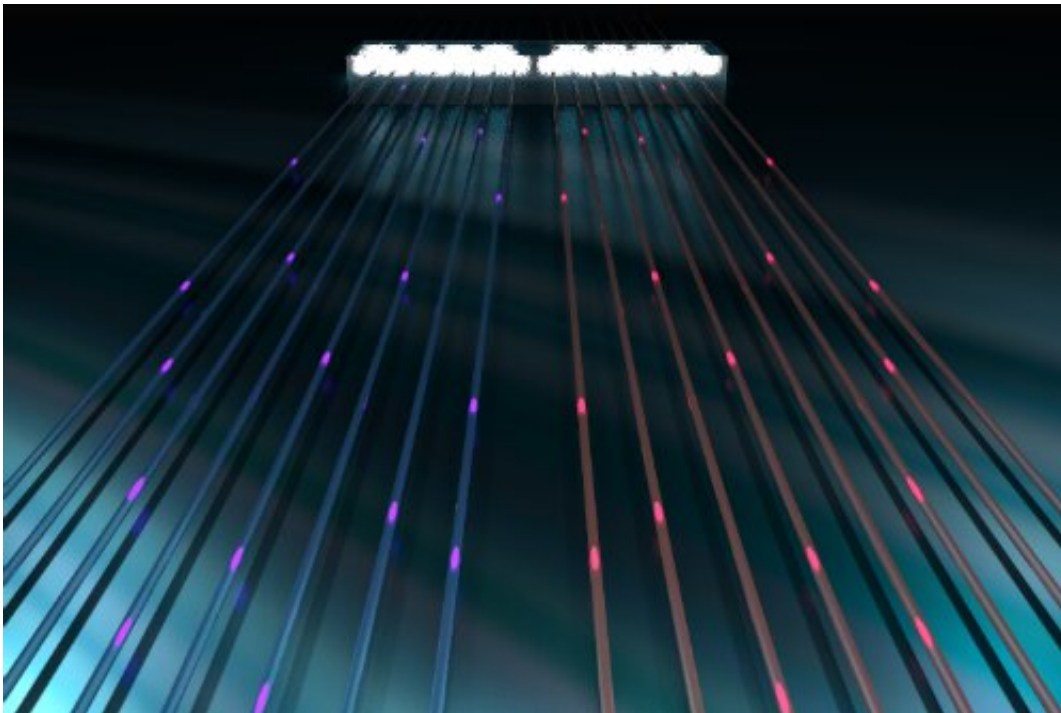


Generation and sampling of quantum states of light in a silicon chip

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By exploring complex integrated circuits, photonic states can be generated and processed at larger scales. Credit: Dr Stefano Paesani, University of Bristol

Scientists from the University of Bristol and the Technical University of Denmark have found a promising new way to build the next generation of quantum simulators combining light and silicon micro-chips.

In the roadmap to develop [quantum](#) machines able to compete and

overcome classical supercomputers in solving specific problems, the scientific community is facing two main technological challenges.

The first is the capability of building large quantum circuits able to process the information on a massive scale, and the second is the ability to create a large number of single quantum particles that can encode and propagate the [quantum information](#) through such circuits.

Both these two requirements need to be satisfied in order to develop an advanced quantum technology able to overcome classical machines.

A very promising platform to tackle such challenges is [silicon](#) quantum photonics. In this technology, the information carried by photons, single particle of lights, is generated and processed in silicon micro-chips.

These devices guide and manipulate light at the nanoscale using integrated waveguides—the analogue of optical fibres at the nanometre-scale.

Crucially, the fabrication of photonic chips requires the same techniques used for fabricating electronic micro-chips in the semiconductor industry, making the fabrication of quantum circuits at a [massive scale](#) possible.

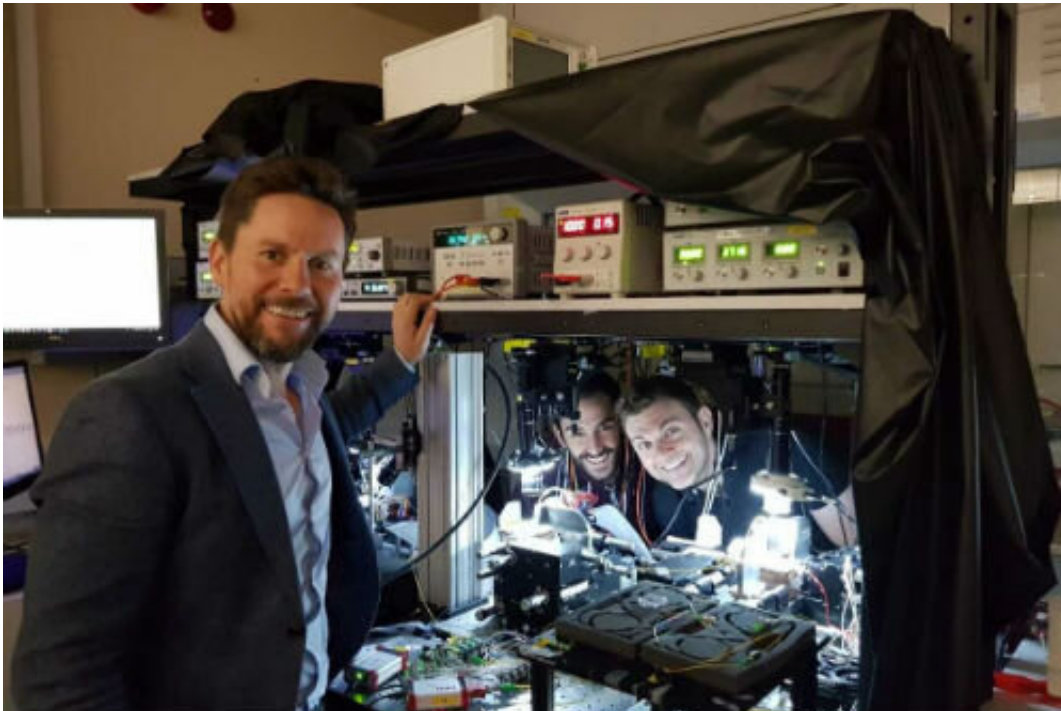
In the University of Bristol's Quantum Engineering Technology (QET) Labs, the team have recently demonstrated silicon photonic chips embedding quantum interferometers composed of almost a thousand optical components, orders of magnitude higher than what was possible just few years ago.

However, the big question that remained unanswered was if these devices were also able to produce a number of photons large enough to perform useful quantum computational tasks. The Bristol-led research,

published today in the journal *Nature Physics*, demonstrates that this question has a positive answer.

By exploring recent technological developments in silicon quantum photonics, the team have demonstrated that even small-scale silicon photonic circuits can generate and process a number of photons unprecedented in integrated photonics.

In fact, due to imperfections in the circuit such as photon losses, previous demonstrations in integrated photonics have been mostly limited to experiments with only two photons generated and processed on-chip, and only last year, four-photon experiments were reported using complex circuitry.



Researchers at QETLabs working on silicon quantum photonics experiments. From left to right: Professor Anthony Laing, Dr Stefano Paesani and Dr Raffaele Santagati. Credit: University of Bristol

In the work, by improving the design of each integrated component, the team show that even simple circuits can produce experiments with up to eight photons, double than the previous record in integrated photonics. Moreover, their analysis shows that by scaling up the circuit complexity, which is a strong capability of the silicon platform, experiments with more than 20 photons are possible, a regime where photonic quantum machines are expected to surpass the best classical supercomputers.

The study also investigates [possible applications](#) for such near-term photonics quantum processors entering a regime of quantum advantage.

In particular, by reconfiguring the type of optical non-linearity in the chip, they demonstrated that silicon chips can be used to perform a variety of quantum simulation tasks, known as boson sampling problems.

For some of these protocols—for example, the Gaussian Boson Sampling—this new demonstration is a world-first.

The team also demonstrated that, using such protocols, silicon quantum devices will be able to solve industrially relevant problems. In particular, they show how the chemical problem of finding the vibrational transitions in molecules undergoing an electronic transformation can be simulated on our type of devices using Gaussian Boson Sampling.

Lead author Dr. Stefano Paesani from the University of Bristol's Centre for Nanoscience and Quantum Information, said: "Our findings show that photonic quantum simulators surpassing classical supercomputers are a realistic near-term prospect for the silicon quantum photonics platform.

"The development of such quantum machines can have potentially ground-breaking impacts on industrially relevant fields such as chemistry, molecular designing, artificial intelligence, and big-data

analysis.

"Applications include the design of better pharmaceuticals and the engineering of molecular states able to generate energy more efficiently."

Co-author Dr. Raffaele Santagati added: "The results obtained make us confident that the milestone of quantum machines faster than any current classical computers is within reach of the integrated quantum photonics platform.

"While it is true that also other technologies have the capability to reach such regime, for example trapped ions or superconducting systems, the photonics approach has the unique advantage of having the near-term applications we investigated. The [photonic](#) path, although perilous, is set, and is very much worth pursuing."

Professor Anthony Laing, Associate Professor of Physics at Bristol, supervised the project. He said: "In quadrupling the number of photons both generated and processed in the same chip, the team have set the scene for scaling up quantum simulators to tens of photons where performance comparisons with today's standard computing hardware become meaningful."

More information: Stefano Paesani et al. Generation and sampling of quantum states of light in a silicon chip, *Nature Physics* (2019). [DOI: 10.1038/s41567-019-0567-8](https://doi.org/10.1038/s41567-019-0567-8)

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