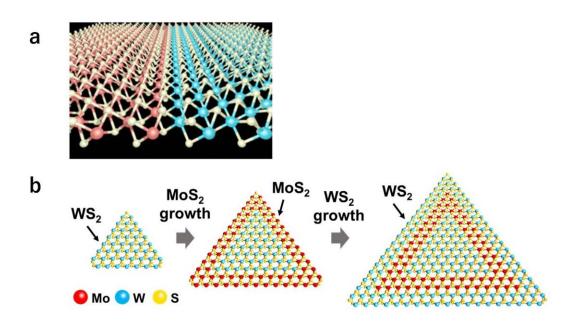


Atomic 'patchwork' using heteroepitaxy for next-generation semiconductor devices

July 1 2019



(a) Red and blue are different metal atoms; yellow are chalcogen atoms. (b) Newly developed procedure. Different TMDC precursors are fed in sequentially to grow crystalline domains with different composition. Credit: Tokyo Metropolitan University

Researchers from Tokyo Metropolitan University have grown atomically thin crystalline layers of transition metal dichalcogenides (TMDCs) with varying composition over space, continuously feeding in different types



of TMDC to a growth chamber to tailor changes in properties. Examples include 20-nanometer strips surrounded by TMDCs with atomically straight interfaces and layered structures. They also directly probed the electronic properties of these heterostructures; potential applications include electronics with unparalleled power efficiency.

Semiconductors are indispensable; silicon-based integrated circuits underpin the operation of all things digital, from discrete devices like computers, smartphones and home appliances to control components for every possible industrial application. A broad range of scientific research has been directed to the next steps in semiconductor design, particularly the application of novel materials to engineer more compact, efficient circuitry that leverages the quantum mechanical behavior of materials at the nanometer-length scale. Of special interest are materials with a fundamentally different dimensionality; the most famous example is graphene, a two-dimensional lattice of carbon atoms which is atomically thin.

Transition metal dichalcogenides (or TMDCs) are promising candidates for incorporation into new semiconductor devices. Composed of transition metals like molybdenum and tungsten and a chalcogen (or Group 16 element) like sulfur or selenium, they can form layered crystalline structures whose properties change drastically when the metallic element is changed, from normal metals to semiconductors, even to superconductors. By controllably weaving domains of different TMDCs into a single heterostructure (made of domains with different composition), it may be possible to produce atomically thin electronics with distinct, superior properties to existing devices.

A team led by Dr. Yu Kobayashi and Associate Professor Yasumitsu Miyata from Tokyo Metropolitan University has been at the cutting edge of efforts to create two-dimensional heterostructures with different TMDCs using vapor-phase deposition, the deposition of precursor



material in a vapor state onto a surface to make atomically flat crystalline layers. One of the biggest challenges they faced was creating a perfectly flat interface between different domains, an essential feature for getting the most out of these devices. Now, they have succeeded in engineering a continuous process to grow well-defined crystalline strips of different TMDCs at the edge of existing domains, creating strips as thin as 20nm with a different composition. Their new process uses liquid precursors which can be sequentially fed into a growth chamber; by optimizing the growth rate, they were able to grow heterostructures with distinct domains linked perfectly over atomically straight edges. They directly imaged the linkage using scanning tunneling microscopy (STM), finding excellent agreement with *first-principles* numerical simulations of what an ideal interface should look like. The team used four different TMDCs, and also realized a layer-on-layer heterostructure.

By creating atomically sharp interfaces, electrons may be effectively confined to one-dimensional spaces on these 2-D devices, for exquisite control of electron transport and resistivity as well as optical properties. The team hopes that this may pave the way to devices with unparalleled energy efficiency and novel optical properties.

More information: Yu Kobayashi et al, Continuous Heteroepitaxy of Two-Dimensional Heterostructures Based on Layered Chalcogenides, *ACS Nano* (2019). DOI: 10.1021/acsnano.8b07991

Provided by Tokyo Metropolitan University

Citation: Atomic 'patchwork' using heteroepitaxy for next-generation semiconductor devices (2019, July 1) retrieved 23 April 2024 from https://phys.org/news/2019-07-atomic-patchwork-heteroepitaxy-next-generation-semiconductor.html



This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.