

Researchers image perfectly smooth sidesurfaces of 3-D silicon crystals with a scanning tunneling microscope

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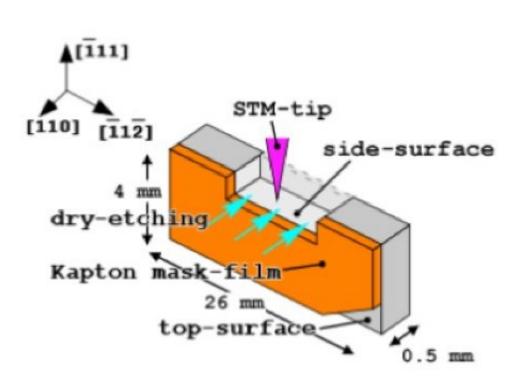


Figure.1. A schematics of a Si(110) sample with a Kapton film mask: dry etching from the (110) top-surface and STM-tip approaching to the (-111) side-surface. Credit: Osaka University

A research collaboration between Osaka University and the Nara Institute of Science and Technology for the first time used scanning tunneling microscopy (STM) to create images of atomically flat side-



surfaces of 3-D silicon crystals. This work helps semiconductor manufacturers continue to innovate while producing smaller, faster, and more energy-efficient computer chips for computers and smartphones.

Our computers and smartphones each are loaded with millions of tiny <u>transistors</u>. The processing speed of these devices has increased dramatically over time as the number of transistors that can fit on a single <u>computer chip</u> continues to increase. Based on Moore's Law, the number of transistors per chip will double about every 2 years, and in this area it seems to be holding up. To keep up this pace of rapid innovation, computer manufacturers are continually on the lookout for new methods to make each transistor ever smaller.



Figure.3. Spatial-derivative STM images with $200x200 \text{ nm}^2$ at Vs = +1.5 V.



Flat terraces become brighter and edges darker. The downstairs direction runs from left ((110) top-surface) to right ((-1-10) back-surface). Credit: Osaka University

Current microprocessors are made by adding patterns of circuits to flat silicon wafers. A novel way to cram more transistors in the same space is to fabricate 3-D-structures. Fin-type field effect transistors (FETs) are named as such because they have fin-like silicon structures that extend into the air, off the surface of the chip. However, this new method requires a silicon crystal with a perfectly flat top and side-surfaces, instead of just the top surface, as with current devices. Designing the next generation of chips will require new knowledge of the atomic structures of the side-surfaces.



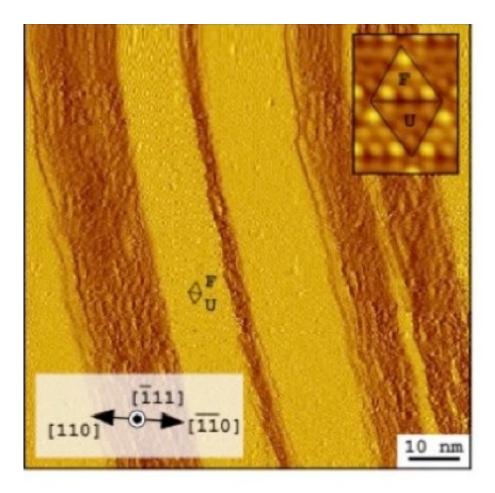


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Now, researchers at Osaka University and the Nara Institute of Science and Technology report that they have used STM to image the sidesurface of a silicon crystal for the first time. STM is a powerful technique that allows the locations of the individual <u>silicon</u> atoms to be seen. By passing a sharp tip very close to the sample, electrons can jump across the gap and create an electrical current. The microscope



monitored this current, and determined the location of the atoms in the sample.

"Our study is a big first step toward the atomically resolved evaluation of transistors designed to have 3-D-shapes," study coauthor Azusa Hattori says.

To make the side-surfaces as smooth as possible, the researchers first treated the crystals with a process called reactive ion etching. Coauthor Hidekazu Tanaka says, "Our ability to directly look at the side-surfaces using STM proves that we can make artificial 3-D structures with near-perfect atomic surface ordering."

More information: Haoyu Yang et al. Direct observation for atomically flat and ordered vertical {111} side-surfaces on three-dimensionally figured Si(110) substrate using scanning tunneling microscopy, *Japanese Journal of Applied Physics* (2017). DOI: 10.7567/JJAP.56.111301

Provided by Osaka University

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