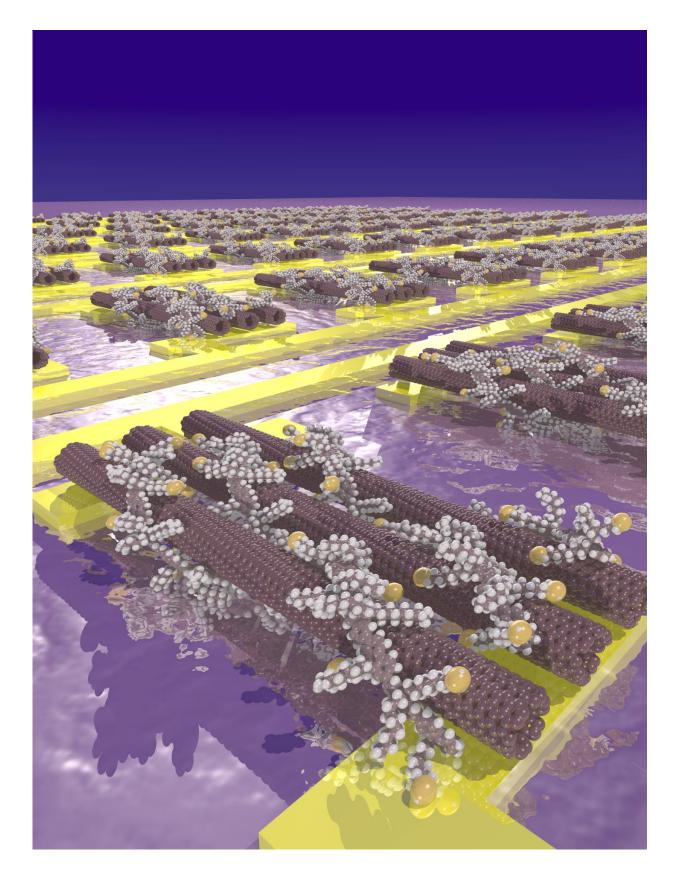


Carbon nanotubes self-assemble into tiny transistors

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Artist's impression of carbon nanotubes wrapped in polymers with thiol side chains (yellow spheres) and assembled on gold electrodes. Credit: Arjen Kamp

Carbon nanotubes can be used to make very small electronic devices, but they are difficult to handle. University of Groningen scientists, together with colleagues from the University of Wuppertal and IBM Zurich, have developed a method to select semiconducting nanotubes from a solution and make them self-assemble on a circuit of gold electrodes. The results were published in the journal *Advanced Materials* on 5 April.

The results look deceptively simple: a self-assembled transistor with nearly 100 percent purity and very high electron mobility. But it took ten years to get there. University of Groningen Professor of Photophysics and Optoelectronics Maria Antonietta Loi designed polymers which wrap themselves around specific <u>carbon</u> nanotubes in a solution of mixed tubes. Thiol side chains on the polymer bind the tubes to the gold electrodes, creating the resultant transistor.

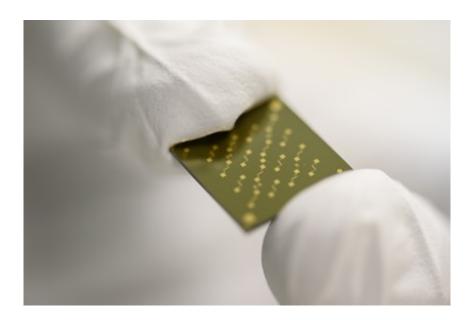
Patent

'In our previous work, we learned a lot about how polymers attach to specific carbon nanotubes', Loi explains. These nanotubes can be depicted as a rolled sheet of graphene, the two-dimensional form of carbon. 'Depending on the way the sheets are rolled up, they have properties ranging from semiconductor to semi-metallic to metallic.' Only the semiconductor tubes can be used to fabricate transistors, but the production process always results in a mixture.

'We had the idea of using polymers with thiol side chains some time ago', says Loi. The idea was that as sulphur binds to metals, it will direct polymer-wrapped nanotubes towards gold electrodes. While Loi was



working on the problem, IBM even patented the concept. 'But there was a big problem in the IBM work: the polymers with thiols also attached to metallic nanotubes and included them in the transistors, which ruined them.'



Credit: University of Groningen

Solution

Loi's solution was to reduce the thiol content of the polymers, with the assistance of <u>polymer</u> chemists from the University of Wuppertal. 'What we have now shown is that this concept of bottom-up assembly works: by using polymers with a low concentration of thiols, we can selectively bring semiconducting nanotubes from a solution onto a circuit.' The sulphur-gold bond is strong, so the nanotubes are firmly fixed: enough even to stay there after sonication of the transistor in organic solvents.

The <u>production process</u> is simple: metallic patterns are deposited on a



carrier, which is then dipped into a solution of carbon nanotubes. The electrodes are spaced to achieve proper alignment: 'The tubes are some 500 nanometres long, and we placed the electrodes for the transistors at intervals of 300 nanometres. The next transistor is over 500 nanometres away.' The spacing limits the density of the <u>transistors</u>, but Loi is confident that this could be increased with clever engineering.

'Over the last years, we have created a library of polymers that select semiconducting nanotubes and developed a better understanding of how the structure and composition of the polymers influences which carbon nanotubes they select', says Loi. The result is a cheap and scalable production method for nanotube electronics. So what is the future for this technology? Loi: 'It is difficult to predict whether the industry will develop this idea, but we are working on improvements, and this will eventually bring the idea closer to the market.'

More information: Vladimir Derenskyi, Widianta Gomulya, Wytse Talsma, Jorge Mario Salazar-Rios, Martin Fritsch, Peter Nirmalraj, Heike Riel, Sybille Allard, Ulrich Scherf, Maria A. Loi: On-chip chemical self-assembly of semiconducting Single-Walled Carbon Nanotubes (SWNTs): towards robust and scale invariant SWNTs transistors. *Advanced Materials*, online 5 April 2017. DOI: 10.1002/adma.201606757

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