

Bridging nanotube gaps enhances performance of electronic devices

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Electrical and computer engineering professor Joseph Lyding and graduate student Jae Won Do led a research team to develop a new method of soldering gaps between carbon nanotubes, a new type of transistor. Credit: L. Brian Stauffer



A more effective method for closing gaps in atomically small wires has been developed by University of Illinois researchers, further opening the doors to a new transistor technology.

Led by electrical and computer engineering professor Joseph Lyding and graduate student Jae Won Do, the Illinois team published its results in the journal *ACS Nano*.

Silicon-based <u>transistors</u> have been the foundation of modern electronics for more than half a century, allowing for the precise control of electronic signals throughout circuits. A new <u>transistor technology</u>, carbon nanotube wires, shows promise in replacing silicon because it can operate ten times as fast and is more flexible – but it has an important gap to cross.

"The connection between the nanotubes is highly resistive and results in slowing the operation of the transistor down," Lyding said. "When electrons go past that junction, they dissipate a lot of energy."

The resistance results in heat pooling at the junctions between the tubes, providing researchers with the perfect opportunity to "solder" these connections using a material that reacts with heat to deposit metal across the junctions. Once the current runs through, the deposited metal reduces the junction resistance, effectively stopping the energy loss.

Until now, the problem has been finding a realizable approach to applying the heat-reactive materials. In 2013, Lyding and Do used a <u>vacuum chamber</u> to apply a gaseous chemical to metallize the junctions. The new technique, the subject of the new paper, takes a different route by applying a thin layer of solution, made from compounds that contain the metal needed to solder the junctions together.





The heat produced at carbon-nanotube junctions causes metallic material in the solution to deposit onto the junctions, soldering them. Credit: Joseph Lyding

"Our new technique is much simpler. It involves fewer steps and it's more compatible to existing technology," Do said. "We're getting similar improvements to what we got from the gaseous method, only now we can experiment with the capabilities of other materials that aren't gases, which will let us improve the transistors' performance even more."

To expand the technique to encompass more materials, Lyding's group teamed with Eric Pop, an adjunct professor of electrical and computer engineering at the U. of I. and an expert on <u>carbon nanotube</u> synthesis



and transfer, as well as with chemistry professor Greg Girolami.

Do said the <u>new technique</u> is transferable to the current manufacturing equipment silicon transistor manufacturers are using.



Credit: Joseph Lyding

"With this method, you just send current through the nanotubes and that heats the junctions. From there, chemistry occurs inside that layer, and then we're done. You just have to rinse it off," Lyding said. "You don't



need a custom, expensive vacuum chamber."

The next step for the team is to start looking at compounds for the <u>junctions</u> that help to amplify the current even more.

"Now that we see this effect, how do we get to the next level? How do we improve by another order of magnitude? We're advancing this work as we speak, with chemicals that have been synthesized specifically for speed," Lyding said.

More information: "Solution-Mediated Selective Nanosoldering of Carbon Nanotube Junctions for Improved Device Performance." *ACS Nano*, Article ASAP <u>DOI: 10.1021/nn505552d</u>

Provided by University of Illinois at Urbana-Champaign

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