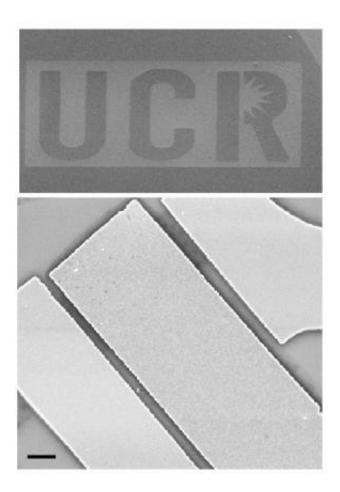


Advancing graphene for post-silicon computer logic: Researchers pioneer new approach for graphene logic circuits

September 3 2013



This is the scanning electron microscopy image of graphene device used in the study. The scale bar is one micrometer. The UCR logo next to it is implemented with etched graphene. Credit: University of California, Riverside



A team of researchers from the University of California, Riverside's Bourns College of Engineering have solved a problem that previously presented a serious hurdle for the use of graphene in electronic devices.

Scanning electron microscopy image of <u>graphene</u> device used in the study. The scale bar is one micrometer. The UCR logo next to it is implemented with etched graphene.

Graphene is a single-atom thick carbon crystal with unique properties beneficial for electronics including extremely high electron mobility and phonon <u>thermal conductivity</u>. However, graphene does not have an energy <u>band gap</u>, which is a specific property of <u>semiconductor</u> <u>materials</u> that separate electrons from holes and allows a transistor implemented with a given material to be completely switched off.

A transistor implemented with graphene will be very fast but will suffer from leakage currents and <u>power dissipation</u> while in the off state because of the absence of the energy band gap. Efforts to induce a bandgap in graphene via <u>quantum confinement</u> or surface functionalization have not resulted in a breakthrough. That left scientists wondering whether graphene applications in <u>electronic circuits</u> for information processing were feasible.

The UC Riverside team – Alexander Balandin and Roger Lake, both electrical engineering professors, Alexander Khitun, an adjunct professor of electrical engineering, and Guanxiong Liu and Sonia Ahsan, both of whom earned their Ph.Ds from UC Riverside while working on this research – has eliminated that doubt.

"Most researchers have tried to change graphene to make it more like conventional semiconductors for applications in <u>logic circuits</u>," Balandin said. "This usually results in degradation of graphene properties. For example, attempts to induce an energy band gap commonly result in



decreasing <u>electron mobility</u> while still not leading to sufficiently large band gap."

"We decided to take alternative approach," Balandin said. "Instead of trying to change graphene, we changed the way the information is processed in the circuits."

The UCR team demonstrated that the negative differential resistance experimentally observed in graphene field-effect transistors allows for construction of viable non-Boolean computational architectures with the gap-less graphene. The negative differential resistance – observed under certain biasing schemes – is an intrinsic property of graphene resulting from its symmetric band structure. The advanced version of the paper with UCR findings can be accessed at <u>http://arxiv.org/abs/1308.2931</u>.

Modern digital logic, which is used in computers and cell phones, is based on Boolean algebra implemented in semiconductor switch-based circuits. It uses zeroes and ones for encoding and processing the information. However, the Boolean logic is not the only way to process information. The UC Riverside team proposed to use specific currentvoltage characteristics of graphene for constructing the non-Boolean logic architecture, which utilizes the principles of the non-linear networks.

The graphene transistors for this study were built and tested by Liu at Balandin's Nano-Device Laboratory at UC Riverside. The physical processes leading to unusual electrical characteristics were simulated using atomistic models by Ahsan, who was working under Lake. Khitun provided expertise on non-Boolean logic architectures.

The atomistic modeling conducted in Lake's group shows that the negative differential resistance appears not only in microscopic-size graphene devices but also at the nanometer-scale, which would allow for



fabrication of extremely small and low power circuits.

The proposed approach for graphene circuits presents a conceptual change in graphene research and indicates an alternative route for graphene's applications in information processing according to the UC Riverside team.

Provided by University of California - Riverside

Citation: Advancing graphene for post-silicon computer logic: Researchers pioneer new approach for graphene logic circuits (2013, September 3) retrieved 23 May 2024 from <u>https://phys.org/news/2013-09-advancing-graphene-post-silicon-logic-approach.html</u>

This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.