

Curiosity rover's recovery moving forward

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This artist concept features NASA's Mars Science Laboratory Curiosity rover, a mobile robot for investigating Mars' past or present ability to sustain microbial life. Image credit: NASA/JPL-Caltech

(Phys.org) —NASA's Mars rover Curiosity continues to move forward with assessment and recovery from a memory glitch that affected the rover's A-side computer. Curiosity has two computers that are redundant of one another. The rover is currently operating using the B-side computer, which is operating as expected.

Over the weekend, Curiosity's mission operations team continued testing and assessing the A-side computer's memory.



"These tests have provided us with a great deal of information about the rover's A-side memory," said Jim Erickson, deputy project manager for the Mars Science Laboratory/Curiosity mission at NASA's Jet Propulsion Laboratory, Pasadena, Calif. "We have been able to store new data in many of the memory locations previously affected and believe more runs will demonstrate more memory is available."

Two software patches, targeting onboard memory allocation and vehicle safing procedures, are likely to be uplinked later this week. After the software patches are installed, the mission team will reassess when to resume full mission operations.

Controllers switched the rover to a redundant onboard computer, the rover's "B-side" computer, on Feb. 28 when the "A-side" computer that the rover had been using demonstrated symptoms of a corrupted memory location. The intentional side swap put the rover, as anticipated, into minimal-activity safe mode. Curiosity exited <u>safe mode</u> on Saturday, March 2, and resumed using its high-gain antenna the following day.

The cause for the A-side's memory symptoms remains to be determined.

Provided by NASA

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