

New chip technology makes way for exploding number of internet users

November 10 2010, By Mike Corrado



New IBM technology for making networking chips promises speedier downloads for consumers. IBM will make the chips for companies who supply networking gear such as routers, switches and cellular base stations, at its advanced East Fishkill, New York, semiconductor plant, shown above. (PRNewsFoto/IBM)

IBM announced a new chip-making technology that can be used to create advanced semiconductors that can keep pace with the exploding number of internet-connected devices and the tidal wave of data they are generating.

The Cu-32 Custom Logic offering employs unique IBM technology -- designed by IBM Research -- to dramatically increase the [memory capacity](#) and processing speeds of chips used in fiber-optic and [wireless networks](#), and in such gear as routers and switches. The technology can

help manufacturers and network operators handle the data deluge driven by consumers' appetites for smart phones and other Web-connected devices.

Systems using chips made with Cu-32 for example, can result in:

- Cellular infrastructure that can move one year's worth of text messages (six trillion, worldwide in 2010) in less than ten seconds
- A consumer downloading a feature-length film on a smart phone in less than ten seconds; or a HD version in under a minute
- Routers that can stream every motion picture ever produced in less than one minute

The number of people using the internet has doubled in the past five years, with two billion logging-on in 2010. Smart phones, game consoles, digital TVs, GPS devices and MP3 players are among the consumer gadgets that now ride the internet. As the world's infrastructure gets further digitized, connected and monitored, vast arrays of machine-to-machine sensors are also beginning to use the internet to transmit data on commuter traffic, buildings' energy usage or the health of newborn infants, for example. Manufacturers of communications infrastructure will increasingly need breakthrough semiconductor technologies such as Cu-32 to keep up with the demand to secure, store and move an ever-growing amount of web traffic.

Embedded Memory, Made in IBM Labs, Key to Breakthrough Performance

IBM's embedded DRAM technology provides the most dense on-chip dynamic memory available today, enabling more than 1Gb of memory on a single [chip](#). IBM eDRAM performance has advanced to a point

where it can replace conventional on-chip static memory (SRAM) in many applications, taking up 60% less space on the chip, and consuming up to 90% less power.

"IBM's Cu-32 technology with ARM advanced physical IP enables chip-makers to get powerful system-on-a-chip solutions quickly to market," said Simon Segars, executive vice president and general manager, ARM physical IP division. "Our collaboration with IBM allows both companies to advance the state-of-the-art in the low-power embedded [semiconductors](#) that will help create next-generation networks."

A suite of new high-speed serial cores (HSS) give Cu-32 advanced capabilities to network with more than a dozen different interface standards. Additionally, IBM's silicon-on-insulator (SOI) process helps improve energy efficiency in chips using Cu-32. Since its invention by IBM in 1998, more than 100 million SOI chips have been shipped, powering the newest generation of video games and enabling a wide range of enhanced communications applications. More than twenty of the world's leading semiconductor makers, tool makers and industry suppliers are members of the SOI Industry Consortium, setting the course of future SOI innovation.

"By any measure – from the growing number of mobile users to the explosion we're seeing in data -- network traffic will grow at a pace we haven't seen before," said Mark Ireland, VP, Semiconductor Products, IBM. "Cu-32, our most advanced Custom Logic offering, with the industry's best eDRAM and high speed serial links will provide our infrastructure partners the lead they need to create next-generation networks."

Technical Features of Cu-32 Design Kit

IBM's High Speed Serial (HSS) cores were developed to provide

industry-leading jitter performance and equalization support for enhanced system performance with the lowest possible bit error ratio. IBM is an active member of the Optical Internetworking Forum committee and is leading the effort to define interface standards for networking communication applications.

Cu-32 offers the industry's first set of HSS cores in 32nm SOI technology including:

- 15G Backplane core supporting 16G Fibre Channel standard
- 15G Chip-to-Chip core supporting low-power optical and chip-to-chip applications
- 28G Backplane core supporting 32G Fibre Channel standard
- 6G standards core supporting PCI-Express Gen1 & Gen2 standards
- PCI-Express Gen3 core supporting PCI-Express Gen1, Gen2, and Gen3 standards

IBM, as the first provider of eDRAM technology in a custom-logic design system continues to expand its eDRAM offering with a compiler that can create more than 3,000 configurations. This flexibility enables smarter silicon solutions with memory optimized for a wide range of applications from high-end servers and networking applications to game processors.

- IBM's eDRAM offering features the fastest and densest eDRAM memory in the industry, achieving up to 600 MHz of random cycle performance while using up to ten times less standby power than conventional SRAM.
- IBM's trench-based eDRAM technology is optimized to deliver high performance and low power, while avoiding many of the process complexities of alternative MIM-cap-based eDRAM cells.

IBM's high-k metal gate (HKMG) SOI technology can provide up to 25% chip performance improvement, up to 30% improved energy efficiency with up to twice the density compared to 45 nm SOI technology, allowing chips built with the Cu-32 process to address an ever wider range of devices and applications.

Design kits for standard cell libraries, memory compilers, eDRAM and supporting Fibre Channel HSS standards are available now with additional HSS standards availabilities slated for end of 2010. IBM's accreditation as a Trusted Supplier enables government sponsored program access to Cu-32 offering.

More information: [www.itu.int/ITU-D/ict/material...
FactsFigures2010.pdf](http://www.itu.int/ITU-D/ict/material...FactsFigures2010.pdf)

Provided by IBM

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