Elpida Develops Industry's Smallest 2-Gigabit DDR Mobile RAM

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Elpida Memory, Japan's leading global supplier of Dynamic Random Access Memory (DRAM), today announced that it had developed a 2-gigabit DDR Mobile RAMTM using a 40nm process. The new Mobile RAM has a chip size of less than 50mm2, which is the smallest chip using 40nm-generation advanced DRAM process technology at the mass production stage.

In the market for DRAM used in handheld mobile devices, the need for DRAM products that can conserve greater amounts of space and power is unrelenting given the ongoing evolution of small and lightweight mobile phones, smartphones, tablets and other handheld devices. In recognition of this need, Elpida has commercialized 2-gigabit DDR Mobile RAM that is the smallest 40nm process chip in the DRAM industry and features lower power consumption through optimizing circuit and layout design and use of its own unique design methods. The newly developed Mobile RAM is Elpida's latest eco-friendly DRAM contribution in support of today's advanced mobile device functionality.

Customers who have used Elpida's 50nm process 1-gigabit products can now expect to find that the new 40nm process 2-gigabit product offers double the amount of memory density without any change in the space needed for chip mounting. Also, the new 2-gigabit product uses less than half the power needed for two 1-gigabit chips.

At present, demand for 2-gigabit DRAM for smartphones and other mobile devices is rapidly growing. With its new Mobile RAM, Elpida

plans to meet customer need by ramping up production as fast as possible to high-volume levels. Sample shipments are expected to begin in June 2010 with mass production likely to start in July. Elpida's Hiroshima Plant is scheduled to handle all production.

Source: Elpida