

Micron, Intel try out 50 nm NAND memory

July 25 2006

Semiconductor giants Micron and Intel said Tuesday they were sampling the first NAND flash memory chips built on 50-nanometer processing technology.

The samples come from their companies' IM Flash Technologies joint venture and have a capacity of 4 gigabits.

Such 50 nm devices are expected to play a key role in new-generation consumer electronics and personal computers. The two companies said the NAND (Not AND) chip-market segment will be worth as much as \$309 billion by the end of the decade.

"Our entry into the NAND flash business has been an incredibly fast ramp," said Intel Vice President Brian Harrison. "We're seeing very high demand across multiple flash densities. Working with Micron, we are poised to transition quickly to the 50 nm process technology and beyond."

Copyright 2006 by United Press International

Citation: Micron, Intel try out 50 nm NAND memory (2006, July 25) retrieved 29 April 2024 from <https://phys.org/news/2006-07-micron-intel-nm-nand-memory.html>

<p>This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.</p>
--