

Intel First With 65nm NOR Flash Memory Chips

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Intel Corporation is the first to sample NOR multi-level cell flash memory chips at 1-gigabit density using its advanced 65-nanometer (nm) process technology. Intel's NOR Flash memory chips are used in devices such as cell phones to manage critical phone operations, handle Personal Information Management data and to store photos, music and videos.

Intel's handset OEM customers will benefit from a common flash architecture that will simplify the migration from 90nm to 65nm process technology.

“With these offerings, Intel continues to lead in providing the industry's most advanced NOR flash memory for the mainstream handset market segment,” said Brian Harrison, Intel vice president and general manager

of the Flash Memory Group. “Our 65nm process technology will improve flash performance to enable the next generation of handsets that deliver new and enhanced capabilities for end-users.”

Samples will be available to customers late in the second quarter.

Intel’s first production of microprocessors built on 65nm technology started in the second half of 2005. The company is currently shipping mobile, desktop, server and embedded processors.

Source: Intel

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