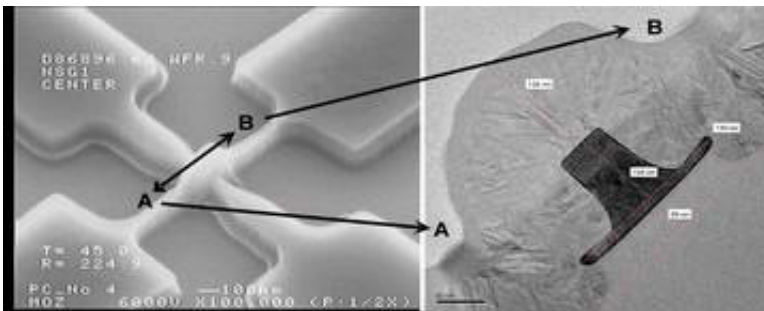


Vertical transistor architecture breaks through CMOS scaling barriers

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Freescape Semiconductor has demonstrated a breakthrough transistor that overcomes many of the design and manufacturing challenges associated with vertical multi-gate devices.

Image: ITFET Technology. Left: The double gate vertical planar thin body surface; Right: ITFET image of the channel region with multiple surface orientation.

The invention, called the Inverted T Channel-Field Effect Transistor (ITFET) device, features the industry's first ever combination of vertical and planar thin body structures within a single transistor. The technology hastens delivery of a new breed of dramatically smaller, higher performing semiconductors that require less power.

Traditional CMOS devices deploy transistors onto the surface of the silicon in a planar — or horizontal — fashion. In recent years new device architectures have emerged, featuring vertical transistors that utilize multiple sides of the silicon. Vertical transistors are appealing in part because they reduce leakage and provide higher drive current — functions of having more than one gate to control the device. Multiple gates pack more computing power into less space and reduce power consumption.

But vertical transistors present fundamental design and manufacturing challenges related to mechanically stability, sub-lithographic feature sizes and patterning over tall topographies.

By combining the stability and manufacturability of planar devices with the low leakage and other benefits of vertical devices, Freescale's ITFET bridges the debate on planar versus vertical CMOS devices and offers key advantages of both technologies in a single device.

"Only five years ago, the prevailing consensus of the industry held that vertical devices were impractical," said Freescale Chief Technology Officer Claudine Simson. "Due to Freescale's uncompromising commitment to technology innovation and manufacturing know-how, many vertical device issues that once were considered insurmountable have now been resolved. ITFET represents one of the most innovative and potentially disruptive semiconductor manufacturing advancements since the industry standardized on traditional planar CMOS technology more than 20 years ago."

About ITFET

ITFET offers better manufacturability than FinFET transistors and other vertical devices. It also provides significant advantages over planar thin body devices and other vertical multi-gate designs, including lower

current leakage, easier transistor width proportioning, lower parasitic capacitance and increased on-current.

The vertical and planar regions of the ITFET couple to provide enhanced current capability from an increased channel width without increasing chip area. The unique architecture of the ITFET incorporates silicon in the planar regions below the vertical channels, thereby improving manufacturability by reducing undercut below the vertical channels, reducing parasitic resistance and enhancing the mechanical stability of the vertical channels.

The ITFET device was fabricated using innovative process techniques on 90-nanometer CMOS silicon-on-insulator production equipment at Freescale's Austin Technology & Manufacturing Center. Freescale plans to incorporate ITFET technology in a range of high-end devices beginning at the 45-nm node and beyond.

ITFET is Freescale's latest addition to a growing portfolio of multi-gate technology and related breakthroughs. The company recently announced Multiple Independent Gate Field Effect Transistor (MIGFET) technology that self-aligns two electrically independent gates across a sub-40-nm vertical channel.

Source: Freescale

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