

# Xilinx Ships World's Highest Capacity FPGA Device

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Xilinx, Inc. today announced the immediate availability and shipment of its Virtex-4 LX100 device, the world's highest capacity [FPGA](#). Featuring over 110,000 logic cells and a host of embedded functionality - the LX100 integrates larger designs than any other FPGA available. In addition to its industry-leading capacity and feature set, the Virtex-4 family delivers the highest performance and lowest power consumption of any high density FPGAs.

The LX100 joins the LX25, LX60, and SX35 devices already available in the Virtex-4 family. Xilinx has six times more FPGA devices available at the 90nm-process node than any other vendor and leads the industry in 90nm device options.

With the introduction of the LX100, design engineers now have a flexible, high-performance, low power and cost-effective alternative to ASICs and ASSPs for high complexity designs.

Each Virtex-4 LX100 Device Includes:

- Triple-Oxide 90nm CMOS Process, reducing power consumption by a factor of six as compared to competing 90-nm FPGAs
- Unique ASMBL architecture, optimized for high-performance logic applications
- 96 500MHz XtremeDSP™ Slices, delivering aggregate DSP

performance of 48 GigaMACs per second at record-setting power-efficiency levels of 23uW/MHz

- 4.3 Mbits of 500MHz SmartRAM™, configurable synchronous dual-port Static RAM, featuring integrated FIFO control logic to build fast FIFOs without consuming logic cell resources.
  
- 960 I/Os featuring 1 Gbps performance and ChipSync source synchronous technology with integrated bit and word alignment circuitry and SERDES available on every I/O
  
- 500MHz DCM™ Digital Clock Managers, offering sub-30ps timing resolution and the world's most advanced clock synthesis and timing capabilities
  
- 500MHz high-performance on-chip differential clock networks for highest precision control of skew and duty-cycle

## **About Xilinx Virtex-4 Platform FPGAs**

Enabled by the revolutionary ASMBL (Advanced Silicon Modular Block) architecture, Virtex-4 FPGAs deliver more options than any other FPGA family available today. With more than 100 technical innovations, the Virtex-4 family consists of 17 devices and three domain-optimized platforms: Virtex-4 LX FPGAs optimized for logic-intensive designs, Virtex-4 SX FPGAs optimized for high-performance signal processing, and Virtex-4 FX FPGAs optimized for high-speed serial connectivity and embedded processing. A multi-platform approach makes it possible for customers to select the optimal mix of resources for their application to achieve the highest functionality and breakthrough performance at the lowest cost.

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