

Rackable Systems Announces Adoption of Next Generation Intel Processors with Extended Memory 64 Technology

August 2 2004

New Line of Processors Complements Rackable Systems' Leading Edge Server Designs

Rackable Systems, Inc., a leading provider of servers for large-scale data center deployments, today announced the **adoption of the Intel(R) Xeon(TM) processor** running at 3.60 GHz with 800 MHz system bus and Intel(R) Extended Memory 64 Technology (EM64T), as well as the Intel(R) E7520 chipset for dual processor performance.

"We're thrilled to see this new technology come to market," said Tom Barton, chief executive officer, Rackable Systems. "Coupled with Rackable Systems' thermally efficient server designs, the new line of EM64T processors will enable the High Performance Computing market to leverage platform-level solutions to achieve maximum performance and reliability."

The Intel Xeon processor, featuring Intel Extended Memory 64 Technology, will enable Rackable Systems' customers to maintain compatibility with the vast IA-32 infrastructure of today, while offering increased memory addressability and a transparent upgrade path to tomorrow's 64-bit-enabled software. An expanded 800 MHz system bus and chipsets supporting PCI Express capability increase system bandwidth to critical resources, and result in better performance for both compute and I/O-intensive applications.



"Servers with the Intel(R) Xeon(TM) EM64T use Intel's latest technology for Intel's channel program members. We are excited that resellers such as Rackable Systems are embracing this latest technology in order to provide a larger spectrum of solutions to their customers," said Willy Agatstein, general manager of Intel's Reseller Products Group.

The original press release can be found here.

Citation: Rackable Systems Announces Adoption of Next Generation Intel Processors with Extended Memory 64 Technology (2004, August 2) retrieved 2 May 2024 from <u>https://phys.org/news/2004-08-rackable-intel-processors-memory-technology.html</u>

This document is subject to copyright. Apart from any fair dealing for the purpose of private study or research, no part may be reproduced without the written permission. The content is provided for information purposes only.