

# SST Announces Production of 64-Mbit MPF+ Flash Memory Devices

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SST (Silicon Storage Technology, Inc., NASDAQ: SSTI), a leader in [flash memory](#) technology, announced initial production and availability of its 64-Mbit Multi-Purpose Flash Plus (MPF+) product family. The 64-Mbit MPF+ SST39VF6401 and SST39VF6402 are the latest and largest densities within SST's MPF+ family of products. Ideal for such applications as [mobile phones](#), set-top boxes, [PDAs](#) and networking equipment, SST's 64-Mbit MPF+ flash products offer several key features, including Boot-Block, Security ID, Erase Suspend and Resume, and Hardware Reset. The MPF+ product line, which has been shipping in lower densities since 4Q03, was developed using the company's 0.18-micron, self-aligned SuperFlash technology and offers densities ranging from 16 Mbit to 64 Mbit.

Similar to the MPF products, the MPF+ product line delivers high performance, superior reliability, low-power operation and small sector size. The product family features chip erase operations 1,000 times faster than conventional flash, eliminating bottlenecks and costs in the manufacturing process by enabling customers to erase their flash devices in 40 milliseconds instead of the one or more minutes typical with most flash alternatives. Additionally, the MPF+ devices allow individual sectors to be erased predictably and at least 50 to 100 times faster than conventional flash, thereby reducing power consumption and providing a more responsive end-user experience.

“With the availability of our 64-Mbit MPF+ product line, the highest density flash SST offers today, we are poised to expand our product

portfolio and future revenue stream,” said Jason Feinsmith, senior business director for the Standard Memory Product Group at SST.

The MPF+ product line boasts several significant features that make it an ideal solution for applications such as set-top boxes and mobile phones. These features include: Erase Suspend/Erase Resume, which allows an erase operation to be suspended so that more time-critical operations can be performed before the erase operation is resumed; Boot Block Write-Protection, which allows the boot block area of the flash to be protected by a write-protect pin; Security ID, which offers two 128-bit, one-time programmable registers; and Hardware Reset, which enables system designers to easily reset the entire flash device.

In addition, the MPF+ products come in the industry’s smallest standard package for comparable products. The 64-Mbit devices are offered in a 48-ball TFBGA, 8mm x 10mm package, as well as in a 48-pin TSOP package. Additionally, the MPF+ product family is offered in a very small ComboMemory package that combines the MPF+ flash die with SRAM. All packages are offered in both lead and non-lead (non-Pb) versions.

### **Pricing and Availability**

The 64-Mbit MPF+ (SST39VF6401 and SST39VF6402) devices are available starting this quarter in speeds as fast as 70ns. Pricing for the SST39VF6401 and SST39VF6402 (bottom boot block and top boot block devices, respectively) is \$7.00 in 10K unit quantities.

For more information about SST and its family of MPF+ flash memory products, visit the company’s Web site at [www.sst.com](http://www.sst.com)

<https://phys.org/news/2004-07-sst-production-mbit-mpf-memory.html>

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