

Cypress Expands FLEx72 Family of Industry's Highest-Bandwidth Dual-Port RAMs

July 26 2004

Newest x72-Bit- Wide Dual-Port Devices Address Density and Bandwidth Requirements Of High-Performance Wireless Basestations, Storage and Communications Networks

[Cypress Semiconductor Corp.](#) today announced that it has added two new devices to its industry-leading FLEx72™ family of high-bandwidth dual-port RAMs. Cypress is sampling 4-Mbit (CYD09S72V) and 9-Mbit (CYD04S72V) products rounding out a x72-bit-wide multiport memory portfolio that includes the 18-Mbit FLEx72 device, which is currently in production.

The FLEx families range in density from 64 Kbits to 18 Mbits systems with speeds up to 167 MHz. In addition to the new parts, Cypress unveiled a dual-port product roadmap that includes several industry firsts including the smallest package, highest speed and pin compatibility with next-generation parts which will be available as early as the first half of next year. Cypress's multiport memory portfolio includes over one hundred synchronous and asynchronous dual-port RAMs that are used in a wide variety of applications, including wireless basestations, security and video-editing equipment.

“Dual-Port memories are not one-size-fits-all,” said Geoff Charubin, Managing Director of Marketing for Cypress's Data Communications Division. “The key to being successful in specialty memories is to have a

robust portfolio that provides the flexibility to meet the requirements of a diverse market space. Cypress has taken that concept to the next level by ensuring that not only do we have the right size devices in our portfolio, we also have the industry's best."

High-performance embedded systems such as basestation, enterprise storage, defense-related communications and signal-processing equipment require a high degree of interprocessor connectivity, where large packets of data need to be buffered between two systems or processors with independent clock domains. The challenge is to enable interprocessor connectivity without impeding system performance. The FLEx72 DP family satisfies the interconnect requirements of various applications by enabling system designers to choose the optimal combination of density and bandwidth.

"Bandwidth is critically important for supporting intensive multiprocessing applications such as wireless basestations and VoIP gateways," said Dinesh Ramanathan, Product Marketing Director for Cypress's Specialty Memory Business Unit. "By rounding out the industry's highest-bandwidth dual-port portfolio to include 4-Mbit and 9-Mbit solutions, Cypress has further affirmed its technology leadership position in the system-interconnect market."

The FLEx72 family provides 4, 9 or 18 Mbits of synchronous, pipelined dual-ported memory. Configured as 64K, 128K or 256K x 72-bit-wide devices, the FLEx72 family provides up to 24 Gbps of bandwidth and allows for easy interface to wide buses; operating up to 167 MHz, they provide a synchronous SRAM interface to processors, DSPs, FPGAs and ASICs. The FLEx72 dual-port RAM features low operating voltage (3.3V) and low standby and dynamic power consumption. These devices include a full range of features such as separate byte enables, master reset, mailbox interrupts for port-to-port message passing and an IEEE 1149.1-compatible JTAG interface for improved manufacturability.

The original press release can be found [here](#).

Citation: Cypress Expands FLEx72 Family of Industry's Highest-Bandwidth Dual-Port RAMs (2004, July 26) retrieved 25 April 2024 from <https://phys.org/news/2004-07-cypress-flex72-family-industry-highest-bandwidth.html>

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